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Possible applications of nanoscale CMOS are examined, with a view to better de?ning the likely capabilities of future microelectronic systems. This analysis covers both data processing applications and nondata processing applications such as RF and imaging. Finally, we speculate on the future of CMOS for the coming 15–20 years.

Nanoscale CMOS - Proceedings of the IEEE
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Nanoscale CMOS Abstract: This paper examines the apparent limits, possible extensions, and applications of CMOS technology in the nanometer regime. Starting from device scaling theory and current industry projections, we analyze the achievable performance and possible limits of CMOS technology from the point of view of device physics, device technology, and power consumption.

Nanoscale CMOS - IEEE Journals & Magazine
Nanotechnology promises to open up new ways of scaling CMOS circuits by introducing new materials. For example, a hybrid circuit of CMOS gates and carbon nano-tubes (CNT), NEMS relay logic and emerging memory devices have been proposed for future nano-scale Field Programmable Gate Arrays (FPGAs).

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Three-Dimensional Nanoscale Mapping of State-of-the-Art ...
Abstract: Steep subthreshold swing transistors based on interband tunneling are examined toward extending the performance of electronics systems. In particular, this review introduces and summarizes progress in the development of the tunnel field-effect transistors (TFETs) including its origin, current experimental and theoretical performance relative to the metal-oxide-semiconductor field ...

Low-Voltage Tunnel Transistors for Beyond CMOS Logic ...
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Circuit Design in Nano-Scale CMOS Technologies ...
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ICCAD '05: Proceedings of the 2005 IEEE/ACM International conference on Computer-aided design FinFETs for nanoscale CMOS digital integrated circuits Pages 207–210

FinFETs for nanoscale CMOS digital integrated circuits ...
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Abstract: As complementary metal-oxide-semiconductor (CMOS) technologies are scaled down into the nanometer range, a number of major nonidealities must be addressed and overcome to achieve a successful analog and physical circuit design. The nature of these nonidealities has been well reported in the technical literature.

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