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3. Contd. Global Logic Optimization
b. Further optimization through multi-level, from:
 $W = A + BC + BD$
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By taking common factors: Let $T1 = C + D$
 $W = A + BT1$
 $X = T1 + B$
 $Y = CD + Z = B B CD CD D B CD CD D$
Design Example 2: BCD to Excess 3 Code Converter
Simpler but non-standard Form (no longer SOP, i.e. > 2 logic levels-

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Logic and Computer Design Fundamentals by Mano M Morris

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Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages, synthesis, and verification, this book focuses on the ever-evolving applications of basic computer design concepts with strong connections to real-world technology. Treatment of logic design, digital system design, and computer design. Ideal for self-study by engineers and computer scientists.

Based on the bestselling texts *Digital Logic and Computer Design* (1972) and *Computer Engineering: Hardware Design* (1988), this text presents the fundamentals of hardware design and integrates state-of-the-art techniques and technologies in an easy-to-understand style with abundant use of examples. Students taking introductory courses in digital logic design, computer engineering, or computer hardware design should find this text useful.

Through examples and analogies, *Computational Thinking for the Modern Problem Solver* introduces computational thinking as part of an introductory computing course and shows how computer science concepts are applicable to other fields. It keeps the material accessible and relevant to noncomputer science majors. With numerous color figures, this classroom-tested book focuses on both foundational computer science concepts and engineering topics. It covers abstraction, algorithms, logic, graph theory, social issues of software, and numeric modeling as well as execution control, problem-solving strategies, testing, and data encoding and organizing. The text also discusses fundamental concepts of programming, including variables and assignment, sequential execution, selection, repetition, control abstraction, data organization, and concurrency. The authors present the algorithms using language-independent notation.

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For courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. Digital Design, fifth edition is a modern update of the classic authoritative text on digital design. This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works.

Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design

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of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader’s understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

Digital Computer Design: Logic, Circuitry, and Synthesis focuses on the logical structure, electronic realization, and application of digital information processors. The manuscript first offers information on numerical symbols, fundamentals of computing aids, quantization, representation of numbers in an electronic digital computer, and computer applications. The text then ponders on the nature of automatic computation and Boolean algebra. Discussions focus on the advantages of a Boolean algebraic description of a digital computer; clock pulse generators and timing circuits; sequential switching networks; elements of information processing systems and types of digital computers; and automatic sequencing methods. The book elaborates on circuit descriptions of switching and storage elements and large capacity storage systems. Topics include static magnetic storage, dynamic delay line storage, cathode-ray storage, vacuum tube systems of circuit logic, and magnetic core systems of circuit logic. The publication also examines the system design of GP computers, digital differential analyzer, and the detection and

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correction of errors. The text is a valuable source of data for mathematicians and engineers interested in digital computer design.

This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

For sophomore courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. & Digital Design, fourth edition is a modern update of the classic authoritative text on digital design.& This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the

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CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

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